PAT-NO:

JP401252037A

DOCUMENT-IDENTIFIER: JP 01252037 A

TITLE:

TRANSMITTING ERROR CORRECTING SYSTEM

PUBN-DATE:

October 6, 1989

INVENTOR-INFORMATION:

NAME

YAMAGISHI, TAKASHI

ASSIGNEE-INFORMATION:

NAME

COUNTRY

TOSHIBA CORP

N/A

APPL-NO:

JP63078876

APPL-DATE:

March 31, 1988

INT-CL (IPC): H04L001/16

US-CL-CURRENT: **714/746** 

## ABSTRACT:

PURPOSE: To obtain all correct data for a short time even when the line

quality of a transmitting line is lowered by storing plural transmitting

frames, in which error data are included, and specifying the correct data in

each data by majority.

CONSTITUTION: When it is detected that the error data are included in the

transmitting frame to be transmitted from a transmitting side device,

abnormal responding signal is transmitted to the transmitting side device and

the retransmission of the same transmitting frame is requested. Simultaneously, the transmitting frame to include the data is stored in one

receiving buffer. Thus, each time the transmitting frame, in which

the error data exist, is received, the transmitting frame is successively stored to the receiving buffer. Next, the respective data in the same position to be stored in the respective receiving buffers are taken out and compared. The most data are obtained as the correct data at the position. When the correct data are replaced with the inputted error data of the transmitting frame, the correct data can be obtained in a receiving side. Thus, when the transmitting frame is received in three times in the most satisfactory condition with such a procedure, the error data can be corrected to the correct data.

COPYRIGHT: (C) 1989, JPO&Japio